Electrical Engineering Department Technical Report 97-8-1 University of Delaware August 1997

A Precision Radio Clock for WWV Transmissions

David L. Mills

Abstract

This report describes a software program that functions as a radio clock using shortwave radio signals transmitted by National Institute of Standards and Technology (NIST) radio stations WWV and WWVH. Operated in conjunction with an inexpensive, fixed-frequency shortwave radio, it has nominal timing errors less than 125 μ s when tracking one of the stations and frequency variations less than 0.5 parts-per-million (PPM) when not tracking either station. The clock produces an ASCII timecode that can be used to set the time of another device, such as a computer, as well as precision reference signals that can be used for other purposes, such as to drive laboratory test equipment.

The primary motivation for this report is as an example and case study of optimum demodulator and decoder design using a maximum likelihood approach and matched filter, synchronous detection and soft decision principles. The clock discipline is modelled as a Markov process, with probabilistic state transitions corresponding to a conventional time-of-century clock and the probabilities of received decimal digits. The result is a performance level which results in very high accuracy and reliability, even under conditions when the one-minute beep from the WWV/H signal, normally its most prominent feature, cannot be detected by ear with a sensitive communications receiver.

Keywords: radio-synchronized clock, digital signal processing, maximum likelihood decoding, matched filter receiver

Sponsored by: DARPA Information Technology Office Contract DABT 63-95-C-0046, NSF Division of Network and Communications Research and Infrastructure Grant NCR 93-01002, Northeastern Center for Electrical Engineering Education Contract A303 276-93

Table of Contents

1.	Introd	ductionduction	1
2.	Desig	gn Approach	2
3.	WWV	V/H Signal Design	2
4.	Hardy	ware Functional Description	4
5.	Software Functional Description		5
	5.1	RF Filtering and Sync Extraction	6
	5.2	Subcarrier Demodulation	
	5.3	Digit Decoding	
6.		rmance Analysis	
7.		ured Performance	
8.		lusions	
9.		rences	
10.		Appendix A. Operation Notes	
	10.1	Front Panel Indicators	
	10.2	Program Commands	
	10.3	Debugging and Monitoring Commands	
	10.4 10.5	Signal Generation and Monitoring Operation Notes	
	10.5	List of Figures	24
Figure	a 1	WWV/H Timecode Format	3
_		DSP-93 Hardware Architecture	
Figure			
Figure		DSP Clock Software Architecture	
Figur		400-Hz Bandpass Filter Response	
Figure		150-Hz Lowpass Filter Response	
Figure	e 5.	1-s Matched Filter Output - WWV Signal	7
Figure	e 6.	1-s Matched Filter Output - WWVH Signal	8
Figure	e 7.	100-Hz Subcarrier Envelope	9
Figure	e 8.	1-s Sync Pulse Amplitude	14
Figure	e 9.	1-m Sync Pulse Amplitude	14
Figure	e 10.	100-Hz Subcarrier SNR	15
Figure	e 11.	Digit Likelihood Values	16
Figure	e 12.	Clock Oscillator Frequency	16
Figure	e 13.	Clock Phase Adjustments	17